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/* nitrogen8m.dts - Boundary Devices Nitrogen8M SBC device tree source */

/*
modification history
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29apr20,jge created
*/

/dts-v1/;

#include "prjParams.h"
#include "imx8mq.dtsi"
#include "imx8mq-clocks.dtsi"
#include "imx8mq-iomux.dtsi"

/ {
    model = "Boundary Devices Nitrogen8M SBC - ARMv8";

    aliases
    {
        ethernet0 = &enet0;
        serial0 = &uart1;
    };

    memory
    {
        device_type = "memory";
        reg = <0x0 0x40000000 0x0 0x80000000>;
    };

    /*
    * U-Boot only fixes up bootargs if <bootargs> environment variable is set
    * when VxWorks kernel is configured in independent DTB mode.
    */

    chosen
    {
        bootargs = "enet(0,0)host:vxWorks h=192.168.10.20 e=192.168.10.10:ffffff00
g=192.168.10.1 u=target pw=vxTarget";
        stdout-path = "serial0";
    };
};

&enet0
{
    phy0: ethernet-phy@0
    {
        compatible = "atheros,ar8035";
        reg = <0>;
    };
};

&iomux
{
    iomux_usb2: iomux_usb2
    {
        pin-set = <
            0x060 0x2C8 0x000 0x0 0x0 0x16
        >;
    };
};

```

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};  
&gpio1  
{  
    status = "okay";  
  
    pinmux-0 = <&iomux_usb2>;  
  
    usb_hub_resetb  
    {  
        gpio-hog;  
        gpios = <14>;  
        output-high;  
    };  
};
```